

PATENT APPLICATION

P12843C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: SHI)	
)	
Serial No.: (unassigned))	Group Art Unit: 2819
)	
Parent Serial No. 10/074,200)	
)	
Parent Filing Date: February 12, 2002)	Examiner: J.B. Jeanglaude
)	
For: Parallel to Serial Conversion Device and)	
Method (Amended))	

PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER OF PATENTS
Washington, D.C. 20231

Dear Sir:

Preliminarily to the examination of this application, claiming priority from U.S.
Patent Appl. 10/074,200 filed on February 12, 2002, please enter the following
amendments and remarks:

Amendment

Title:

Please delete the phrase "A (N-1)/N Current Reduction Scheme in an N-to-1 Parallel-to-Serial Conversion" and replace with - - Parallel to Serial Conversion Device and Method - - therefor.